



07/21/00

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Date: July 21, 2000Docket No.: 1152-0263P

Assistant Commissioner for Patents
 Box PATENT APPLICATION
 Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): SAKAGUCHI, Hideaki

For: TESTING DEVICE AND TESTING METHOD FOR SEMICONDUCTOR
 INTEGRATED CIRCUITS

Enclosed are:

- X A specification consisting of 38 pages
- X 5 sheet(s) of Formal drawings
- X An assignment of the invention
- Certified copy of Priority Document(s)
- X Executed Declaration X Original Photocopy
- A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27
- Preliminary Amendment
- Information Disclosure Statement, PTO-1449 and reference(s)

Other _____

The filing fee has been calculated as shown below:

LARGE ENTITY				SMALL ENTITY	
FOR	NO. FILED	NO. EXTRA	RATE FEE		RATE FEE
BASIC FEE	***** ***** *****	***** ***** *****	***** ***** \$690.00 *****	or	**** **** \$345.00 ****
TOTAL CLAIMS	11 - 20 =	0	x18 =\$ 0.00	or	x 9 = \$ 0.00
INDEPENDENT	4 - 3 =	1	x78 =\$ 78.00	or	x 39 = \$ 0.00
MULTIPLE DEPENDENT CLAIM PRESENTED <u>no</u>			+260 = \$ 0.00	or	+130 = \$ 0.00
TOTAL \$ 768.00				TOTAL \$ 0.00	

X A check in the amount of \$ 808.00 to cover the filing fee and recording fee (if applicable) is enclosed.

— Please charge Deposit Account No. 02-2448 in the amount of \$ _____. A triplicate copy of this transmittal form is enclosed.

— No fee is enclosed.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By _____

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